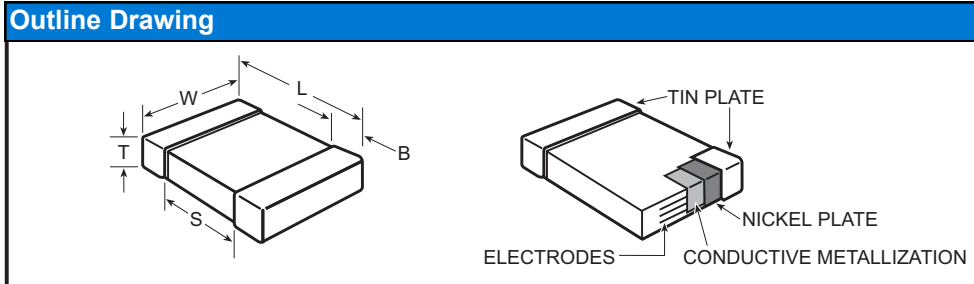


Surface Mount Ceramic Chip Capacitors – C0805 Size - X5R Dielectric



Dimensions – Millimeters (Inches)					
EIA Size Code	Metric Size Code	L Length	W Width	B Bandwidth	S Separation
0805	2012	2.0 (.079) ± 0.2 (.008)	1.25 (.049) ± 0.2 (.008)	0.5 (.02) ± 0.25 (.010)	0.75 (.030)

See below for thickness dimension.

Capacitance Value						
Capacitance Values (µF)	KEMET Part Number	Voltage	Capacitance Tolerance	Thickness mm	Qty* 7" Reel	Qty* 13" Reel
47.0	C0805C476M7PAC	4	M	1.25 (.049) ± 0.20 (.008)	3,000	10,000

* Plastic Tape Only

Electrical Parameters

As detailed in the KEMET Surface Mount Catalog F3102 for X5R, with following specific requirements based on room temperature (25°C) parameters:

- Operating Range: -55°C to +85°C, with no-bias capacitance shift limited to ± 15% over that range.
- Insulation Resistance (IR) measured after 2 minutes at rated voltage @ 25°C: Limit is 500 megohm microfarads or 10000MΩ, whichever of the two is smaller.
- Capacitance and Dissipation Factor (DF) measured at the following conditions. DF Limit is 10%. 120 Hz and 0.5 Vrms.

Soldering Process

These components are suitable for reflow only. All parts incorporate the standard KEMET barrier layer of pure nickel, with an overplate of pure tin to provide excellent solderability as well as resistance to leaching.

Marking

These chips will be supplied unmarked.

In general, the information in the KEMET Surface Mount catalog F3102 applies to these capacitors. The information in this bulletin supplements that in the catalog.

